



Department of Computer Systems Engineering  
Time Table Fall 2014 (3rd Semester)  
wef September 01, 2014

**3rd Semester- Batch-15**

DAYS	Regular	8:00-9:30	9:30-11:00	11:00-12:30	12:30-02:00	2:00-3:30	3:30-5:00
Monday	A	DE (CR-2)	DLD Lab (MP Lab)	DE (CR-3)	OOP (CR-6)		
	B		OOP (CR-6)	OOP (CR-6)	CS-II (CR-2)	CS-II (CR-2)	LA (CR-2)
	C						
Tuesday	A	LA (CR-2)	DLD (CR-2)	LA (CR-4)	CS-II (CR-3)		
	B		CS-II (CR-3)	DE (CR-3)	DLD (CR-2)		
	C			DLD (CR-2)	DLD Lab (MP Lab)	DE (CR-2)	
Wednesday	A	CS-II Lab (EE Lab)		DLD (CR-2)	CS-II (DSP Lab)		
	B	LA (CR-2)	DLD (CR-2)	DLD Lab (MP Lab)			
	C			CS-II (CR-5)	DLD (CR-2)	OOP Lab (Lab 3)	
Thursday	A	OOP Lab (Lab 3)			OOP (CR-6)		
	B		OOP (CR-6)	CS-II Lab (EE Lab)			
	C			OOP (CR-6)	LA (CR-2)	CS-II Lab (EE Lab)	
Friday	A	LA (CR-2)	DE (CR-2)				
	B	OOP Lab (Lab 3)					
	C			DE (CR-3)	BREAK		

Course Abr	Course Code	Course Title	Instructors	Credit Hours
DLD	CSE-202	Digital Logic Design	Dr. Laiq Hasan	3
DLD LAB	CSE-202 L	Digital Logic Design Lab	Engr. Sidra Gul	1
OOP	CSE-201	Data Structure with OOP	Dr. Nasru Minallah	3
OOP LAB	CSE-201 L	Data Structure with OOP Lab	Engr. Kashif Ahmad	1
CS-II	CSE-203	Circuits and Systems-II	Engr. Irfan Ahmad	3
CS-II LAB	CSE-203 L	Circuits and Systems-II Lab	Engr. Atif Sardar	1
LA	BSI-111	Linear Algebra	Dr. Iltaf Hussain	3
DE	BSI-231	Differential Equations	BSI	3

Semester Coordinator